L	Hits	Search Text	DB	Time stamp
Number 2	752	shift adj register with serial with	USPAT	2003/11/21
		parallel with conversion		09:25
3	0	shift adj register with serial adj to adj parallel adj conversion	USPAT	2003/11/21
4	326	shift adj register with serial\$4parallel	USPAT	2003/11/21
5	23	shift adj register with parallel\$4series	USPAT	2003/11/21 09:37
6	7	shift adj register with (parallel\$4series and series\$4parallel) adj conversion	USPAT	2003/11/21
7	0	preshift adj register with error adj (detection or correction)	USPAT	2003/11/21
8	294	shift adj register with error adj   (detection or correction)	USPAT	2003/11/21
9	0	microprocessor with decod\$4 with lighting	USPAT	09:58
10	4	adj device microprocessor with decod\$4 with lighting	USPAT	09:59 2003/11/21
11	1	partial adj shifting with shift adj	USPAT	10:07 2003/11/21
12	3	register   preshift adj register	USPAT	10:04 2003/11/21
13	9		USPAT	10:04 2003/11/21
14	0	lighting multiple adj load with shift adj register	USPAT	10:15 2003/11/21
15	3589	(partial or portion) with shift adj	USPAT	10:16 2003/11/21
16	18	· · · · · · · · · · · · · · · · · · ·	USPAT	10:17 2003/11/21
17	3		USPAT	10:35 2003/11/21
18	3	shift adj register preshift adj register	USPAT	10:33 2003/11/21
19	8	preshift adj register	USPAT; US-PGPUB;	10:34 2003/11/21 10:35
			EPO; JPO; DERWENT; IBM TDB	
20	20	(partial or portion) with fill with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/21 10:39
21	1245	data with portion with shift adj register	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/11/21 10:39
			DERWENT; IBM TDB	
22	29	data with portion with shift adj register with error	USPAT; US-PGPUB;	2003/11/21 10:43
			EPO; JPO; DERWENT; IBM_TDB	
23	43	hold adj register with shift adj register	USPĀT; US-PGPUB; EPO; JPO;	2003/11/21 10:49
			DERWENT; IBM TDB	
24	414	portion adj shift\$4 with shift adj register	USPAT; US-PGPUB;	2003/11/21 10:50
			EPO; JPO; DERWENT;	
25	2	portion adj shift\$4 with shift adj	IBM_TDB USPAT;	2003/11/21
		register with error	US-PGPUB;	10:54
			EPO; JPO; DERWENT;	
	<u> </u>		IBM_TDB	

26	0	portion with error\$free with shift adj register	USPAT; US-PGPUB;	2003/11/21 10:54
			EPO; JPO;	
			DERWENT;	
27	30	annunceur with thise and went the	IBM TDB	0000/11/01
21	30	error\$free with shift adj register	USPAT; US-PGPUB;	2003/11/21
			EPO; JPO;	11:07
			DERWENT;	
			IBM TDB	
28	597	partial with shift adj register	USPAT;	2003/11/21
			US-PGPUB;	11:07
			EPO; JPO;	
			DERWENT;	
29	9	partial with shift adj register with	IBM_TDB USPAT;	2003/11/21
		(fill or load)	USPAT; US-PGPUB;	11:10
		\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	EPO; JPO;	
			DERWENT;	
	-		IBM_TDB	
30	0	multiple adj (load or fill) with shift	USPAT;	2003/11/21
		adj register	US-PGPUB;	11:10
			EPO; JPO;	
			DERWENT; IBM TDB	
31	4	plural adj (load or fill) with shift adj	USPAT;	2003/11/21
	<b>,</b>	register	US-PGPUB;	11:12
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
32	6948	partial with error (checking or	USPAT;	2003/11/21
		detecting) with shift adj register	US-PGPUB;	11:13
	,		EPO; JPO; DERWENT;	
			IBM TDB	
33	0	partial with error adj (checking or	USPAT;	2003/11/21
	_	detecting) with shift adj register	US-PGPUB;	11:13
			EPO; JPO;	
			DERWENT;	
34	144	opported (shocking or detection)	IBM_TDB	2003/11/21
34	144	error adj (checking or detecting) with shift adj register	USPAT; US-PGPUB;	11:50
		Sitte ad teather	EPO; JPO;	11.50
			DERWENT;	
			IBM_TDB	
35	8	incremental with error with shift adj	USPĀT;	2003/11/21
		register	US-PGPUB;	11:53
			EPO; JPO;	
			DERWENT; IBM TDB	
36	0	incremental adj (load or fill) with shift	USPAT;	2003/11/21
		adj register	US-PGPUB;	11:53
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	
37	0	partial adj (load or fill) with shift adj	USPAT;	2003/11/21
		register	US-PGPUB; EPO; JPO;	11:54
			DERWENT;	
			IBM TDB	
38	100	4\$1bit with 8\$1bit with shift adj	USPAT;	2003/11/21
		register	US-PGPUB;	11:57
			EPO; JPO;	
			DERWENT;	
39	3	Asibit with asibit with abise and	IBM_TDB USPAT;	2002/11/01
ر د ا	3	4\$1bit with 8\$1bit with shift adj register with error	USPAT; US-PGPUB;	2003/11/21 12:31
			EPO; JPO;	12.31
			DERWENT;	
			IBM TDB	
				·

		2671065	TIGDAM	2002/11/01
40	2	3671865.pn.	USPAT; US-PGPUB;	2003/11/21 12:44
			EPO; JPO;	12.33
		•	DERWENT;	
	_		IBM_TDB	2222/22/22
41	9	3671865.URPN.	USPAT	2003/11/21
42	11	shift adj register with data adj loss	USPAT;	12:32 2003/11/21
142	*	Shirt adj register with data adj 1035	US-PGPUB;	12:55
			EPO; JPO;	12.00
			DERWENT;	
			IBM_TDB	
43	0	microprocessor with (auxiliary or	USPAT;	2003/11/21
		supplemental) adj hardware adj device	US-PGPUB; EPO; JPO;	12:56
			DERWENT;	
			IBM TDB	
44	5	microprocessor with (auxiliary or	USPĀT;	2003/11/21
		supplemental) adj hardware	US-PGPUB;	12:56
			EPO; JPO;	
			DERWENT;	
45	11907	microprocessor with hardware	IBM_TDB USPAT;	2003/11/21
42	1190/	microprocessor with nardware	US-PGPUB;	12:57
			EPO; JPO;	
		·	DERWENT;	
			IBM_TDB	
46	59		USPAT;	2003/11/21
		hardware	US-PGPUB; EPO; JPO;	13:11
1			DERWENT;	
			IBM TDB	
47	42479	arbitration and control with lighting	USPĀT;	2003/11/21
			US-PGPUB;	13:11
			EPO; JPO;	
			DERWENT;	
48	,	arbitration adj control with lighting	IBM_TDB USPAT;	2003/11/21
40	† ·	albitiation adj control with lighting	US-PGPUB;	13:11
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	0000/11/01
49	11	arbitration with lighting	USPAT; US-PGPUB;	2003/11/21 13:17
			EPO; JPO;	13.17
			DERWENT;	
			IBM_TDB	
50	1	retransmission with lighting with command	USPAT;	2003/11/21
			US-PGPUB;	13:21
	1		EPO; JPO; DERWENT;	
			IBM TDB	
51	545	error with retransmission adj request	USPAT;	2003/11/21
1			US-PGPUB;	13:22
			EPO; JPO;	
	1		DERWENT;	
52	_	error with retransmission adj request	IBM_TDB USPAT;	2003/11/21
1 2		with lighting	US-PGPUB;	13:22
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	2002/11/21
53	4	error with retransmission adj request	USPAT;	2003/11/21 13:23
	1	with control adj signal	US-PGPUB; EPO; JPO;	13.43
			DERWENT;	
			IBM_TDB	
54	0	arq with lighting	USPAT;	2003/11/21
			US-PGPUB;	13:23
			EPO; JPO; DERWENT;	
			IBM TDB	
	L	1	1	

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C C C				
55	2	arq with light\$4	USPAT; US-PGPUB;	2003/11/21 13:25
			EPO; JPO; DERWENT; IBM TDB	
56	3	multiple with (partial or portion) with shift adj register with (load or fill)	USPAT; US-PGPUB;	2003/11/21
		1	EPO; JPO; DERWENT;	13.20
57	2165	(data or signal) with portion with shift	IBM_TDB USPAT;	2003/11/21
		adj register	US-PGPUB; EPO; JPO;	13:29
			DERWENT; IBM TDB	
58	47	(data or signal) with portion with shift adj register with load	USPAT; US-PGPUB;	2003/11/21
		-	EPO; JPO; DERWENT;	
59	0	error adj check adj input with shift adj	IBM_TDB USPAT;	2003/11/21
		register	US-PGPUB; EPO; JPO;	13:33
			DERWENT; IBM TDB	
60	75	error adj check with shift adj register	USPĀT; US-PGPUB;	2003/11/21 14:34
			EPO; JPO; DERWENT;	
61	0	and regrees wrom balance and impac	IBM_TDB USPAT;	2003/11/21
		adj buffer	US-PGPUB; EPO; JPO;	14:35
62			DERWENT; IBM_TDB	
02	0	shift adj register with small\$3 adj input adj buffer	USPAT; US-PGPUB;	2003/11/21 14:35
			EPO; JPO; DERWENT;	
63	0	shift adj register with small\$3 adj input adj register	IBM_TDB USPAT; US-PGPUB;	2003/11/21 14:36
			DS-PGPUB; EPO; JPO; DERWENT;	14:30
64	422	shift adj register with input adj buffer	IBM_TDB USPAT;	2003/11/21
	2	and reference whom triput and parties	US-PGPUB; EPO; JPO;	14:36
			DERWENT; IBM TDB	
65	0	shift adj register with input adj buffer with (staged or staggered)	USPĀT; US-PGPUB;	2003/11/21 14:36
			EPO; JPO; DERWENT;	
66	12	shift adj register with input adj buffer	IBM_TDB USPAT;	2003/11/21
		with (partial or portion)	US-PGPUB; EPO; JPO;	15:12
67	21042	innut ali buss.	DERWENT; IBM_TDB	
67	31843	input adj buffer error with shift adj register	USPAT; US-PGPUB;	2003/11/21 15:15
			EPO; JPO; DERWENT;	
68	0	input adj buffer adj error with shift adj register	IBM_TDB USPAT;	2003/11/21
		1091001	US-PGPUB; EPO; JPO;	15:15
			DERWENT; IBM_TDB	



69	0	input adj stage adj error with shift adj register	USPAT; US-PGPUB;	2003/11/21 15:15
			EPO; JPO; DERWENT; IBM TDB	
70	87237	byte wih error adj checking with shift adj register	USPĀT; US-PGPUB;	2003/11/21 15:16
			EPO; JPO; DERWENT;	
71	1	byte with error adj checking with shift adj register	IBM_TDB USPAT; US-PGPUB;	2003/11/21 15:32
			EPO; JPO; DERWENT;	
72	0	(plural or multiple) adj partial adj	IBM_TDB USPAT;	2003/11/21
		loads with shift adj register	US-PGPUB; EPO; JPO; DERWENT;	15:33
73	2	(plural or multiple) adj partial with	IBM_TDB USPAT;	2003/11/21
		shift adj register	US-PGPUB; EPO; JPO;	15:36
74	113	377/70.ccls.	DERWENT; IBM_TDB USPAT;	2003/11/21
'-			US-PGPUB; EPO; JPO;	15:43
75		wlant add maneial add land with abite	DERWENT; IBM_TDB	2002/11/21
/5	0	plural adj partial adj load with shift adj register	USPAT; US-PGPUB; EPO; JPO;	2003/11/21 15:44
			DERWENT; IBM_TDB	
76	0	partial adj load with shift adj register	USPAT; US-PGPUB; EPO; JPO;	2003/11/21 15:44
			DERWENT; IBM TDB	
77	4	plural adj load with shift adj register	USPAT; US-PGPUB;	2003/11/21 15:45
			EPO; JPO; DERWENT; IBM TDB	
78	0	incomplete adj load with shift adj register	USPAT; US-PGPUB;	2003/11/21 15:45
			EPO; JPO; DERWENT;	
79	0	incomplete adj fill with shift adj	IBM_TDB USPAT; US-PGPUB;	2003/11/21
			EPO; JPO; DERWENT;	
80	. 0	partial adj fill with shift adj register	IBM_TDB USPAT; US-PGPUB;	2003/11/21 15:57
			EPO; JPO; DERWENT;	13.37
81	811	input adj buffer with shift	IBM_TDB USPAT;	2003/11/21
			US-PGPUB; EPO; JPO; DERWENT;	15:58
82	422	input adj buffer with shift adj register	IBM_TDB USPAT;	2003/11/21
			US-PGPUB; EPO; JPO;	16:43
			DERWENT; IBM TDB	

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83	0	multiple adj byte adj shift adj register	USPAT;	2003/11/21
			US-PGPUB;	16:38
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
84	8	multiple adj byte with shift adj register	USPAT;	2003/11/21
			US-PGPUB;	16:38
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
85	139	315/292.ccls.	USPAT;	2003/11/21
			US-PGPUB;	16:43
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
86	493	315/292,294,316.ccls.	USPAT;	2003/11/21
			US-PGPUB;	16:44
			EPO; JPO;	
			DERWENT;	
			IBM TDB	